

FIG. 1
(PRIOR ART)

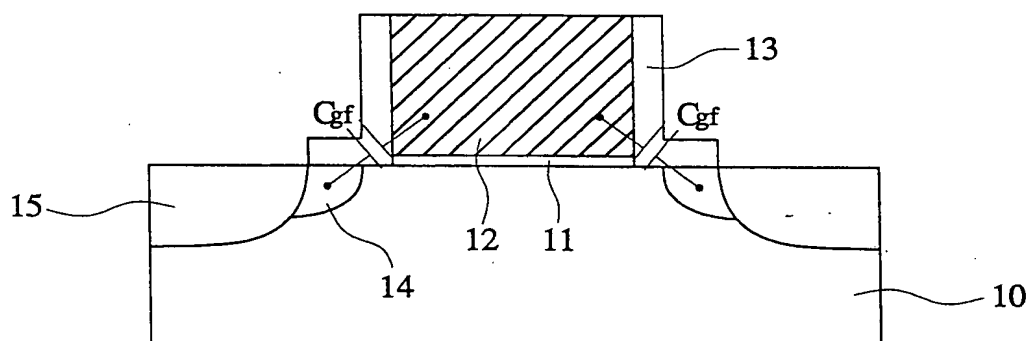


FIG. 2

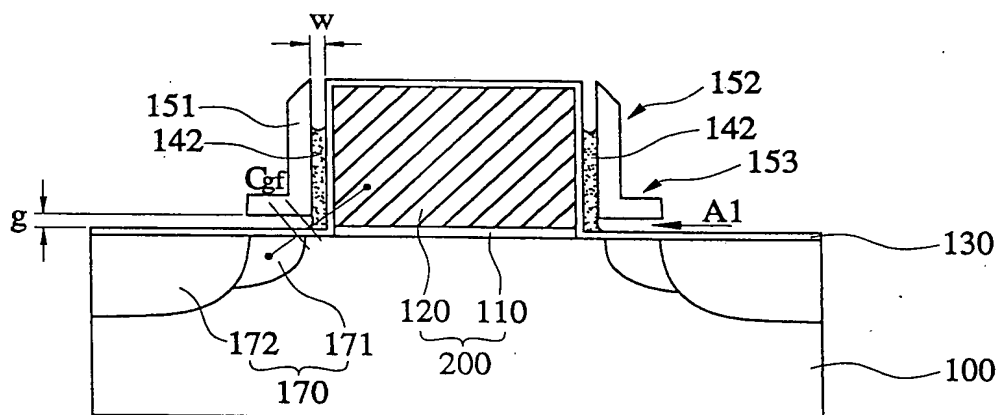


FIG. 3A

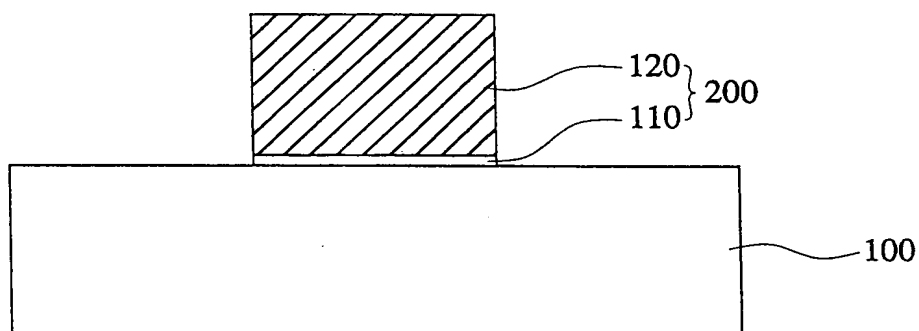


FIG. 3B

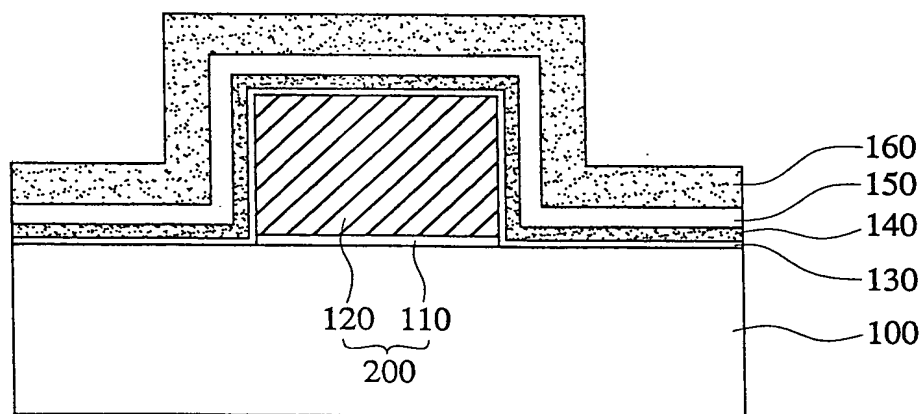


FIG. 3C

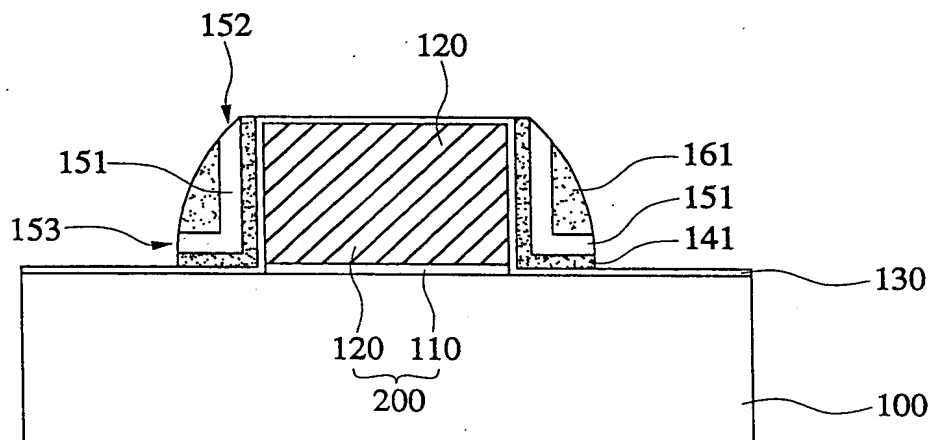
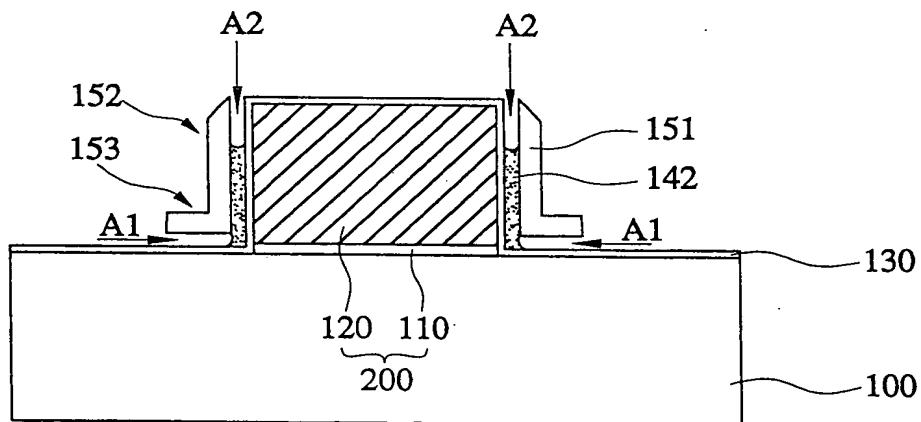


FIG. 3D



SEMICONDUCTOR DEVICE INCLUDING AIR GAP BETWEEN SEMICONDUCTOR SUBSTRATE AND
L-SHAPED SPACER AND METHOD OF FABRICATING THE SAME

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Inventors: Ho-Woo Park et al.

FIG. 3E

